

Electronic Acknowledgement Receipt

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Application Number:	09976004
International Application Number:	
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Title of Invention:	Reducing overhead when setting up multiple virtual circuits using signaling protocols
First Named Inventor/Applicant Name:	Amit S. Phadnis
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)

1	Amendment After Final	resp-8_cSCO-10.pdf	143176 c78b88c9308d961345c40903412d0f1 9fa305e0	no	34
Warnings:					
Information:					
2	Request for Continued Examination (RCE)	RCE_cSCO-010.pdf	30995 069d8ad970970498550cd70298305b64 54d47be	no	3
Warnings:					
This is not a USPTO supplied RCE SB30 form.					
Information:					
3	Fee Worksheet (PTO-06)	fee-info.pdf	8354 b14907001e5684b8decc7c92c79415 ead15889	no	2
Warnings:					
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Total Files Size (in bytes):					182525
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